

09887783_CLS

Most Frequently Occurring Classifications of Patents Returned
From A Search of 09887783 on February 10, 2004

Original Classifications

8 714/726
4 714/718
4 714/733
3 365/201
3 714/724
3 716/4
2 327/199
2 714/727
2 714/736
2 714/738
2 714/741

Cross-Reference Classifications

5 365/201
5 714/724
4 714/733
3 324/765
3 714/726
3 714/738
3 714/739
2 326/16
2 327/291
2 327/299
2 365/195
2 714/718
2 714/727

Combined Classifications

11 714/726
8 365/201
8 714/724
8 714/733
6 714/718
5 714/738
4 714/727
3 324/765
3 327/199
3 714/736
3 714/739
3 716/4
2 326/16
2 327/291
2 327/299

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2 365/195
2 714/719
2 714/731
2 714/741

09887783_CLSTITLES
Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 09887783 on February 10, 2004

11 714/726 (8 OR, 3 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/699 PULSE OR DATA ERROR HANDLING
714/724 .Digital logic testing
714/726 ..Scan path testing (e.g., level sensitive sca
n
design (LSSD))

8 365/201 (3 OR, 5 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/201 .Testing

8 714/724 (3 OR, 5 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/699 PULSE OR DATA ERROR HANDLING
714/724 .Digital logic testing

8 714/733 (4 OR, 4 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/699 PULSE OR DATA ERROR HANDLING
714/724 .Digital logic testing
714/733 ..Built-in testing circuit (BILBO)

6 714/718 (4 OR, 2 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/699 PULSE OR DATA ERROR HANDLING
714/718 .Memory testing

5 714/738 (2 OR, 3 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/699 PULSE OR DATA ERROR HANDLING
714/724 .Digital logic testing
714/738 ..Including test pattern generator

4 714/727 (2 OR, 2 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY

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714/699 PULSE OR DATA ERROR HANDLING
714/724 .Digital logic testing
714/726 ..Scan path testing (e.g., level sensitive scan
n
 design (LSSD))
714/727 ...Boundary scan

3 324/765 (0 OR, 3 XR)
Class 324 : ELECTRICITY: MEASURING AND TESTING
324/500 FAULT DETECTING IN ELECTRIC CIRCUITS AND OF
 ELECTRIC COMPONENTS
324/537 .Of individual circuit component or element
324/765 ..Test of semiconductor device

3 327/199 (2 OR, 1 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
 DEVICES, CIRCUITS, AND SYSTEMS
327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
327/185 .Particular stable state circuit (e.g.,
 tristable, etc.)
327/199 ..Circuit having only two stable states (i.e.,
 bistable)

3 714/736 (2 OR, 1 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
 DETECTION/RECOVERY
714/699 PULSE OR DATA ERROR HANDLING
714/724 .Digital logic testing
714/736 ..Device response compared to expected
 fault-free response

3 714/739 (0 OR, 3 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
 DETECTION/RECOVERY
714/699 PULSE OR DATA ERROR HANDLING
714/724 .Digital logic testing
714/738 ..Including test pattern generator
714/739 ...Random pattern generation (includes
 pseudorandom pattern)

3 716/4 (3 OR, 0 XR)
Class 716 : DATA PROCESSING: DESIGN AND ANALYSIS OF
 CIRCUIT OR SEMICONDUCTOR MASK
716/1 CIRCUIT DESIGN
716/4 .Testing or evaluating

2 326/16 (0 OR, 2 XR)
Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY

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326/16 WITH TEST FACILITATING FEATURE

- 2 327/291 (0 OR, 2 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
327/291 .Clock or pulse waveform generating
- 2 327/299 (0 OR, 2 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
327/291 .Clock or pulse waveform generating
327/299 ..Single clock output with single clock input
or data input
- 2 365/195 (0 OR, 2 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/191 .Signals
365/195 ..Inhibit
- 2 714/719 (1 OR, 1 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/699 PULSE OR DATA ERROR HANDLING
714/718 .Memory testing
714/719 ..Read-in with read-out and compare
- 2 714/731 (1 OR, 1 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/699 PULSE OR DATA ERROR HANDLING
714/724 .Digital logic testing
714/726 ..Scan path testing (e.g., level sensitive sca
n
design (LSSD))
714/731 ...Clock or synchronization
- 2 714/741 (2 OR, 0 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/699 PULSE OR DATA ERROR HANDLING
714/724 .Digital logic testing
714/738 ..Including test pattern generator
714/741 ...Simulation

09887783_LIST
PLUS Search Results for S/N 09887783, Searched February 10, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

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09887783_QUAL

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5323400 82
4097797 82
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5357572 69